

Amendments to the Specifications

Please amend paragraph 37 on page 11 as follows:

[0037] To generate a particular clock signal, several factors are taken into account. The factors include the serial clocking frequency, the desired clocking frequency, the two byte patterns, and the sequence of the byte patterns. Typically, however, the serial clocking frequency and the desired clocking frequency are used as a starting point in determining the other factors. Using the serial clock and the desired clock, the byte patterns can be calculated. Once the byte patterns are known, more calculations may be performed to determine how many instances of each byte pattern are needed to synthesize the desired clock frequency. For example, three instances of pattern A and four instances of pattern B may be needed to synthesize an average clock frequency that is equal to or substantially equal to the desired clocking frequency. Finally, the instances of each byte pattern are arranged in a sequence that instructs the multiplexer which byte patterns to select for serialization. Once the sequence is determined, it may be stored, for example, in control circuitry [[34]] 32.